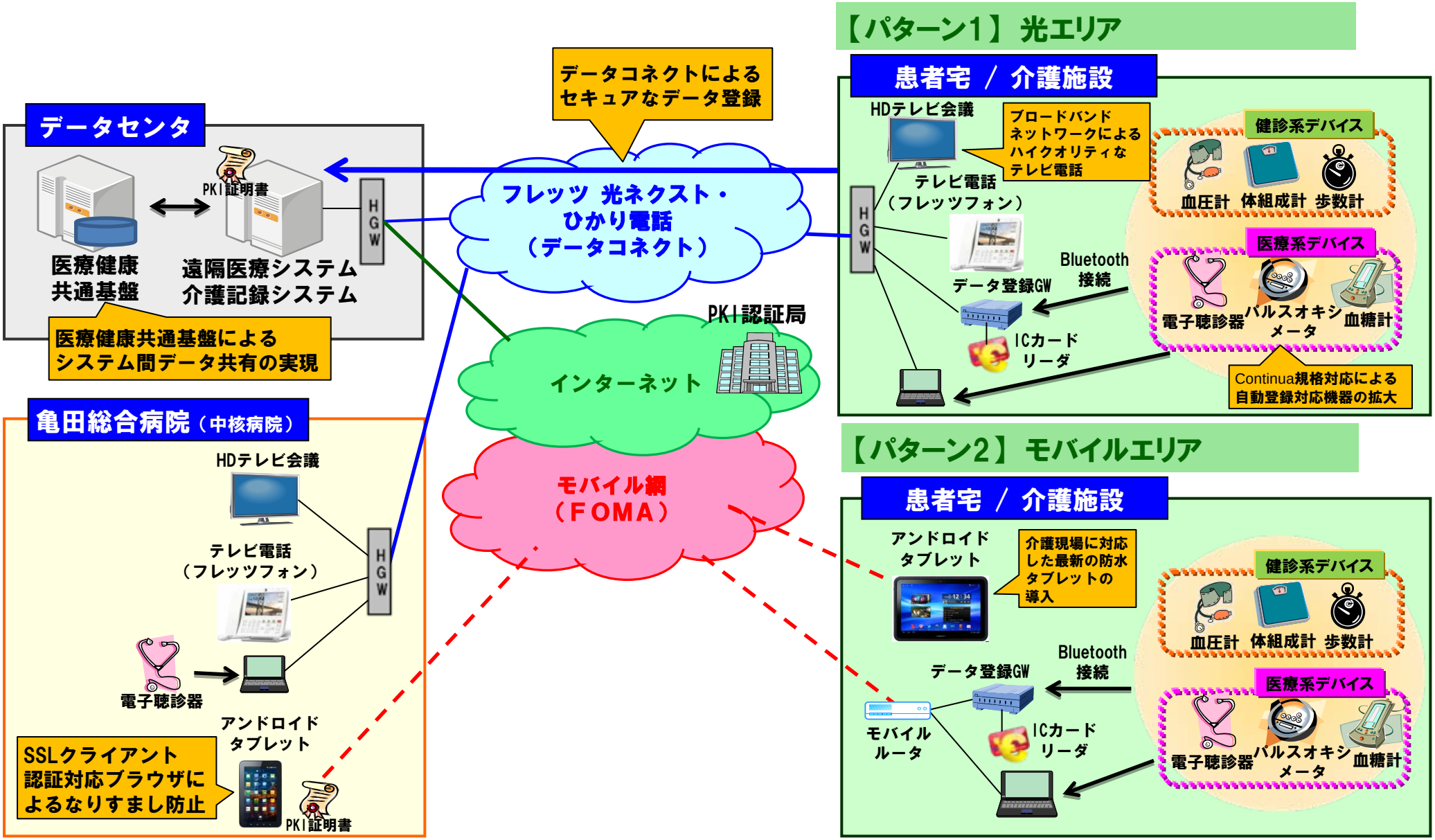【図2】実証におけるシステム概要